

## **REMARKS**

Claims 1-22 remain in the application for consideration of the Examiner.

Reconsideration and withdrawal of the outstanding rejections and objection, are respectfully requested in light of the above amendments and following remarks.

The drawings were objected to.

By the instant amendment, a proposed drawing correction has been submitted.

It is respectfully submitted that the drawings are in full compliance with 37 CFR 1.83.

Turning now to the art rejections, Claim 1 was rejected under 35 U.S.C. § 103 as being unpatentable over Kaku in view of Lane; and Claims 2 and 8 were rejected under 35 U.S.C. § 103 as being unpatentable over Kaku in view of Lane and alleged admitted prior art (AAPA).

These rejections are respectively traversed.

It is respectfully submitted that Kaku does not disclose or suggest the presently claimed invention including the data processor that can determine filter parameters using algorithmically defined relationships among discrete center frequency data, discrete bandwidth data, and discrete gain data such that the plurality of equalizing filters can be re-characterized by the filter parameters in independent Claim 1, and the translating means for translating a desired bandwidth on a desired peak gain and generating the variable multiplier parameter such that the plurality of digital equalizing filters can be re-characterized with a desired multiplier in independent Claim 8.

Applicants agree with the Examiner as evidence by the paragraphing bridges pages 2 and 3 of the Office Action that Kaku does not specify the contents of the parameter and computation methods.

Lane does not disclose or suggest the presently claimed invention including the data processor that can determine filter parameters using algorithmically defined relationships among the discrete center frequency data, discrete bandwidth data, and discrete gain data such that the plurality of equalizing filters can be re-characterized by the filter parameters in independent Claim 1, albeit defined as the translating means for translating the desired bandwidth and the desired peak gain and generating variable multiplier parameter such that the plurality of digital equalizing filters can be re-characterized in independent Claim 8.

The Examiner alleges that Lane discloses center frequency bandwidth and gain data received by referring to element 104 in Figure 2 and the corresponding description at column 5, lines 19-30.

Notwithstanding the comments of the Examiner, Lane discloses at column 5, lines 19-30 that method 100 includes a step for receiving the desired filter control parameters. Preferably the desired filter control parameters are received automatically from a control panel or other mechanism for receiving user input. Such other mechanisms include computer, switches graphic, and user interfaces.

Lane continues by disclosing that it should be noted that a typical graphic equalizer includes a plurality of controls for receiving desire boost/cut values for a plurality of frequency bands. Each frequency band in the graphic equalizer has a particular center frequency. The boost/cut levels are generally the filter control parameters provided and received in step 104.

There is no mention of bandwidth.

Whether or not AAPA discloses an allpass filter and one of ordinary skill in the art would consider modifying the above mentioned claimed subject matter is of no moment since the result in construction would still in no way disclose or suggest the presently claimed invention.

Applicants appreciate the indication that Claims 5-7 and 13-22 are allowed.

Furthermore, Applicants appreciate the indication that if Claims 3-4 and 9-12 were rewritten in independent form including the limitations of the base claim and any intervening claims, these claims would be allowable.

Claims 3 and 4 have been placed in independent form including most of the limitations of Claim 1.

In a similar fashion, Claim 9 has been placed in independent form including a majority of the limitations of Claim 8.

It is respectfully submitted that these claims are now allowable.

In light of the above, it is respectfully submitted that the present application is in condition for allowance, and notice to that effect is respectfully requested.

While it is believed that the instant response places the application in condition for allowance, should the Examiner have any further comments or suggestions, it is respectfully requested that the Examiner contact the undersigned in order to expeditiously resolve any outstanding issues.

To the extent necessary, Applicant petitions for an Extension of Time under 37 CFR 1.136. Please charge any fees in connection with the filing of this paper,

including extension of time fees, to the deposit account of Texas Instruments Incorporated, Account No. 20-0668.

Respectfully submitted,

A handwritten signature in black ink, appearing to read 'WDS', with a long, sweeping horizontal stroke extending to the right.

W. Daniel Swayze, Jr.  
Attorney for Applicant  
Reg. No. 34,478

Texas Instruments Incorporated  
P.O. Box 655474, MS 3999  
Dallas, TX 75265  
(972) 917-5633